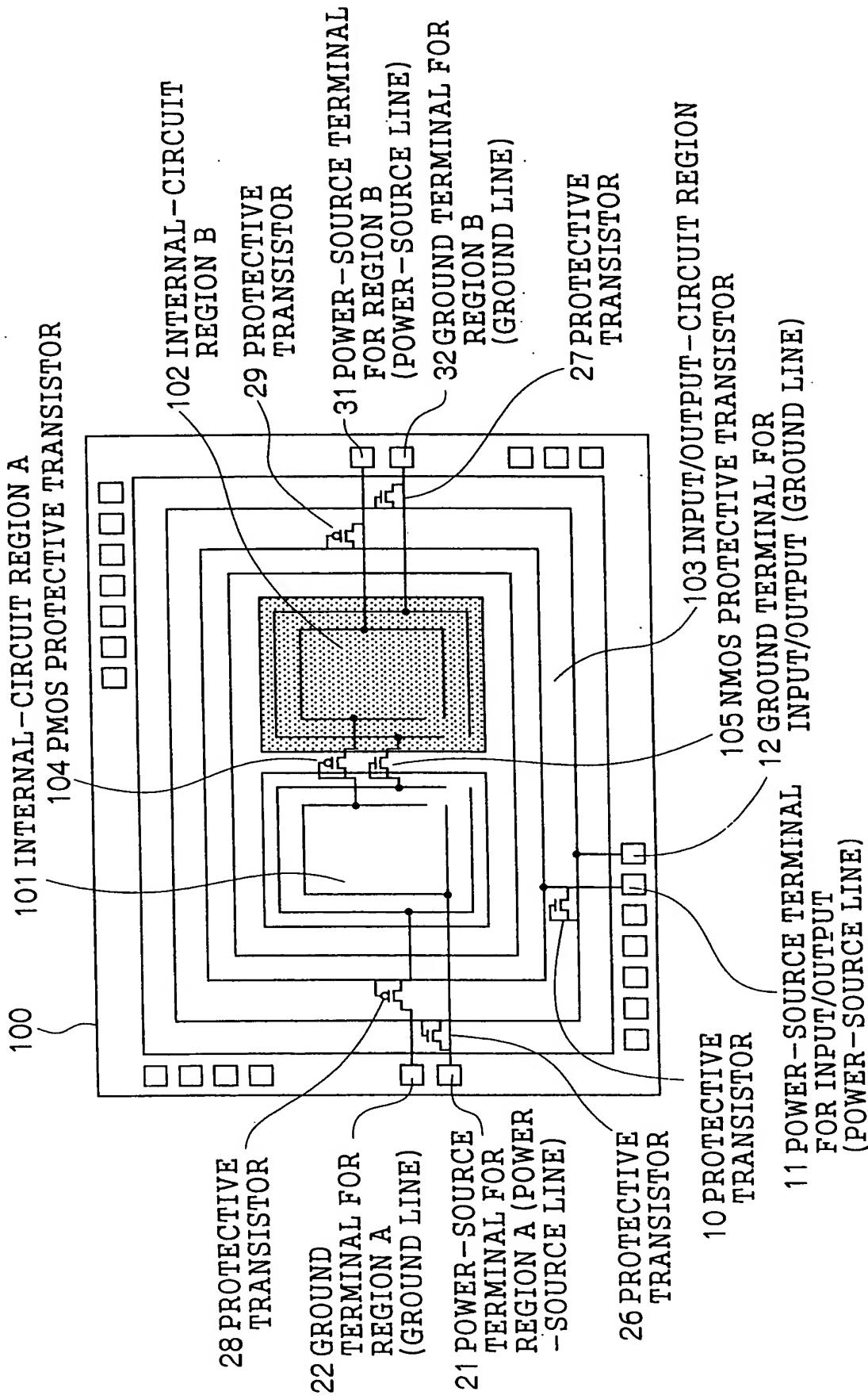


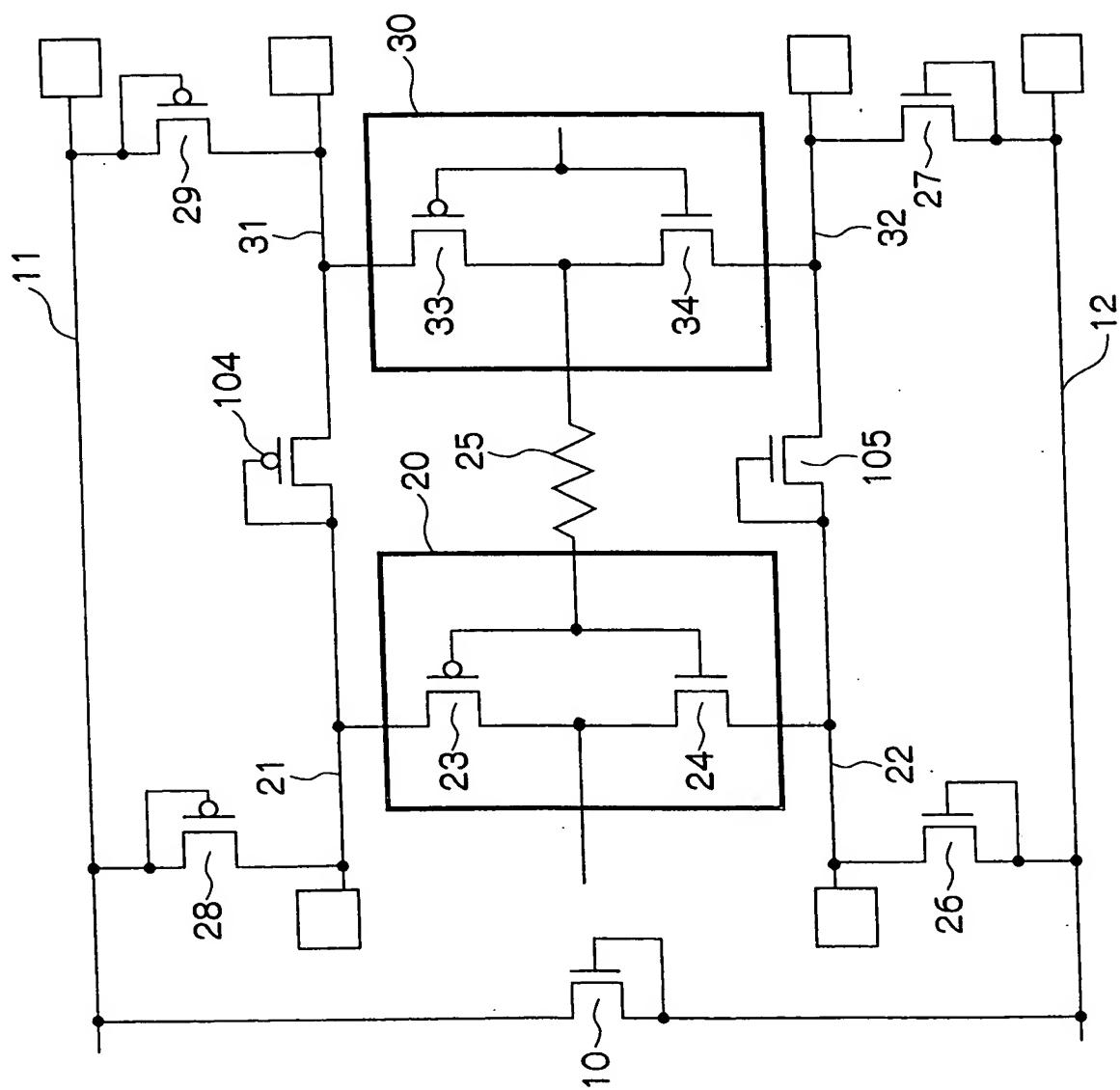
ELECTROSTATIC-BREAKDOWN-PREVENTIVE AND  
PROTECTIVE CIRCUIT FOR SEMICONDUCTOR-DEVICE  
Application No. [New] – Attorney Doc. No. OKI.439D  
Inventor: Katsuhiro KATO

FIG. 1



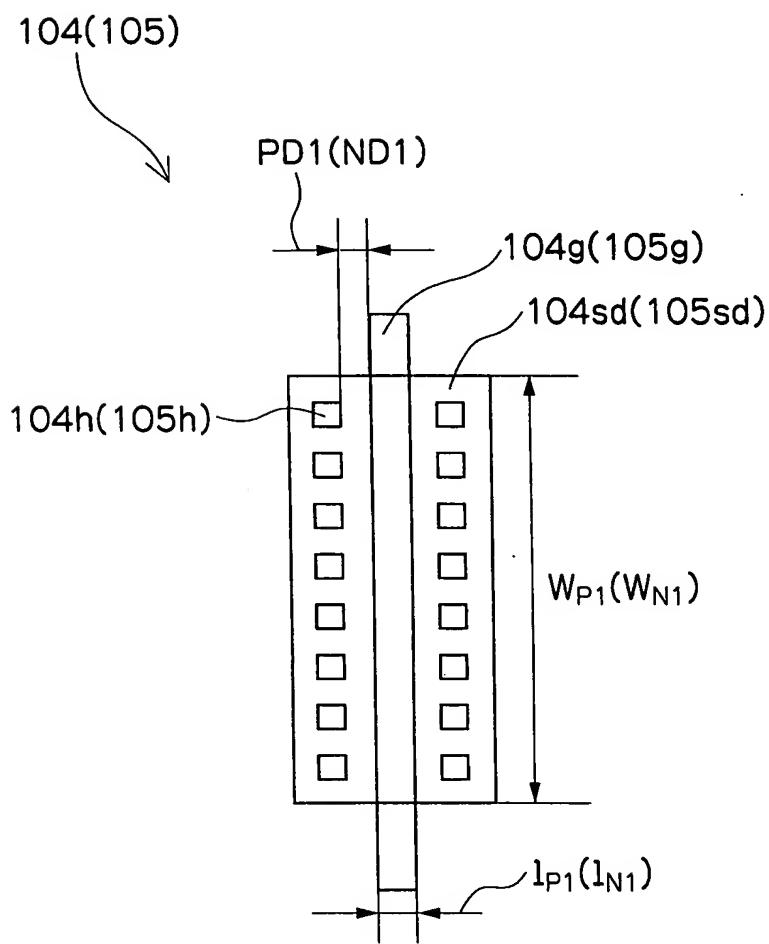
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Application No. [New] – Attorney Doc. No. OKI.439D  
Inventor: Katsuhiro KATO

FIG.2



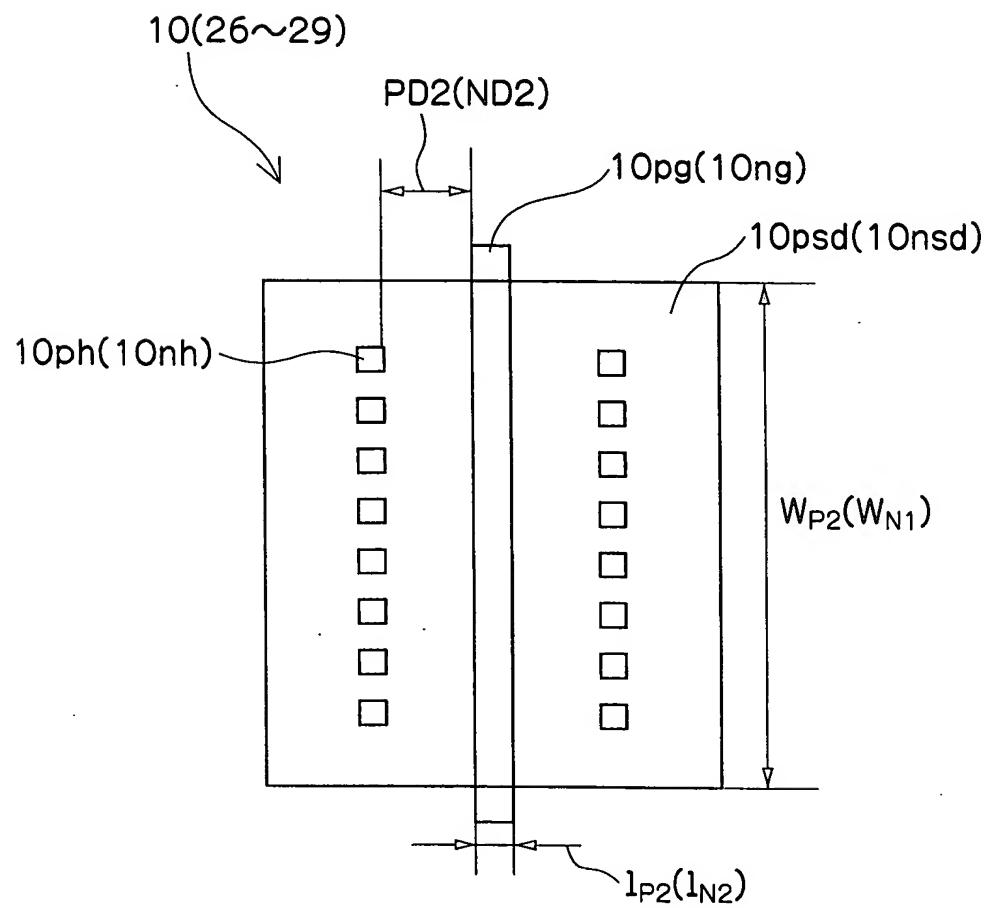
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Inventor: Katsuhiro KATO

FIG.3



ELECTROSTATIC-BREAKDOWN-PREVENTIVE AND  
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FIG.4



ELECTROSTATIC-BREAKDOWN-PREVENTIVE AND  
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Inventor: Katsuhiro KATO

FIG.5

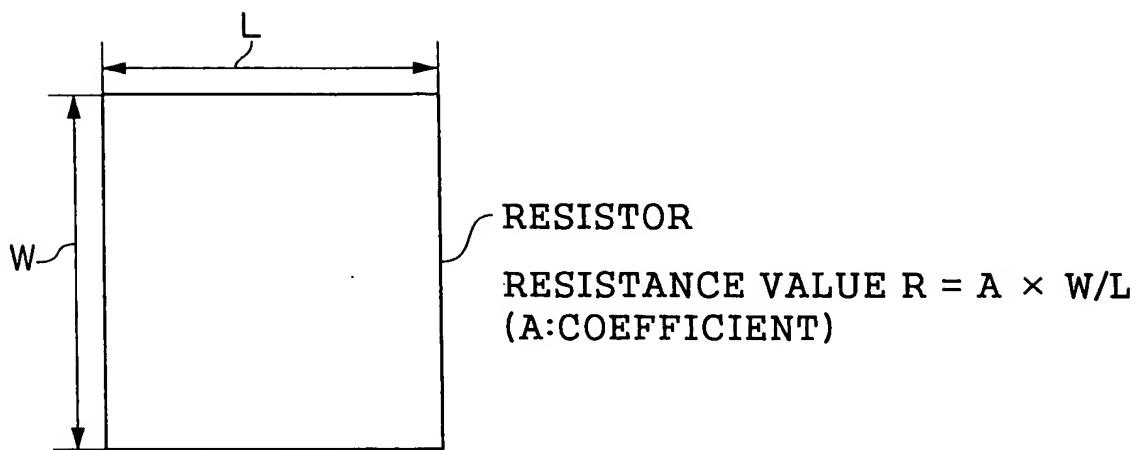


FIG.6

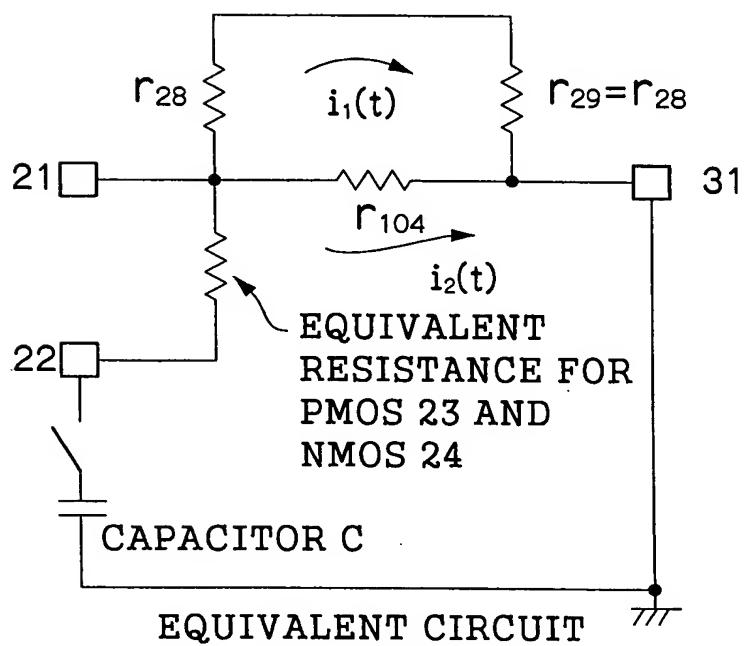


FIG.7

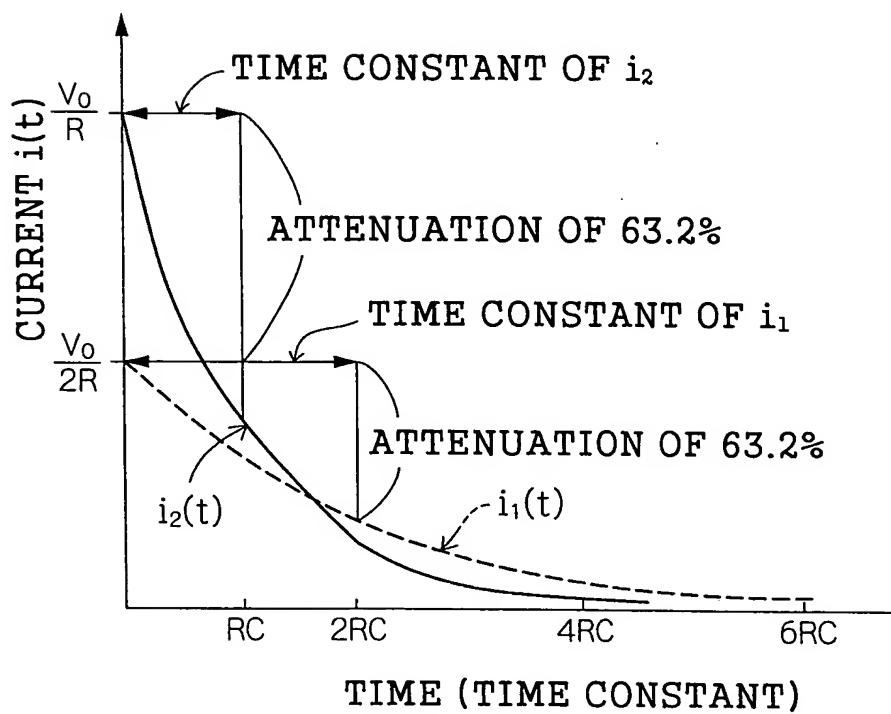
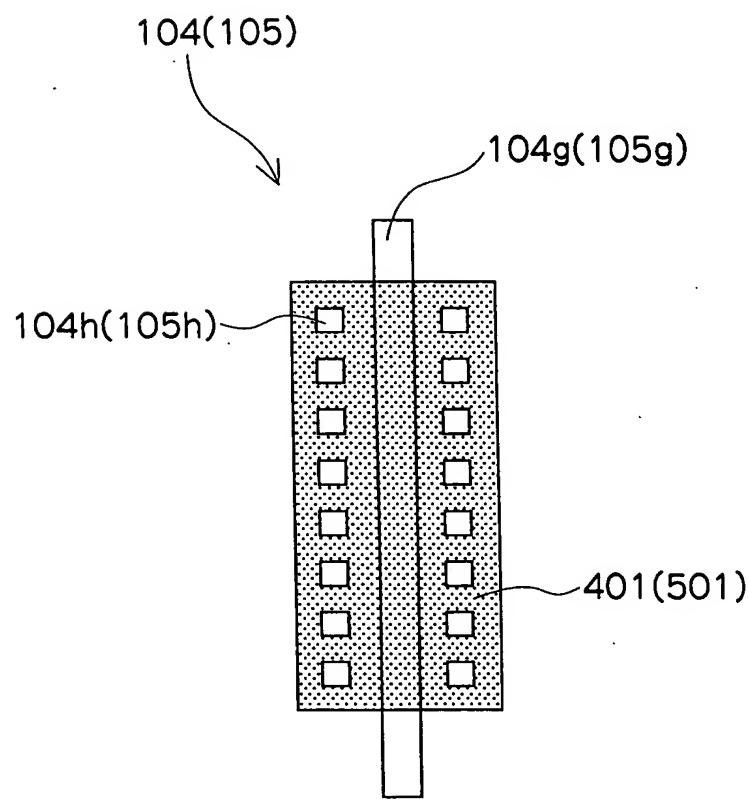
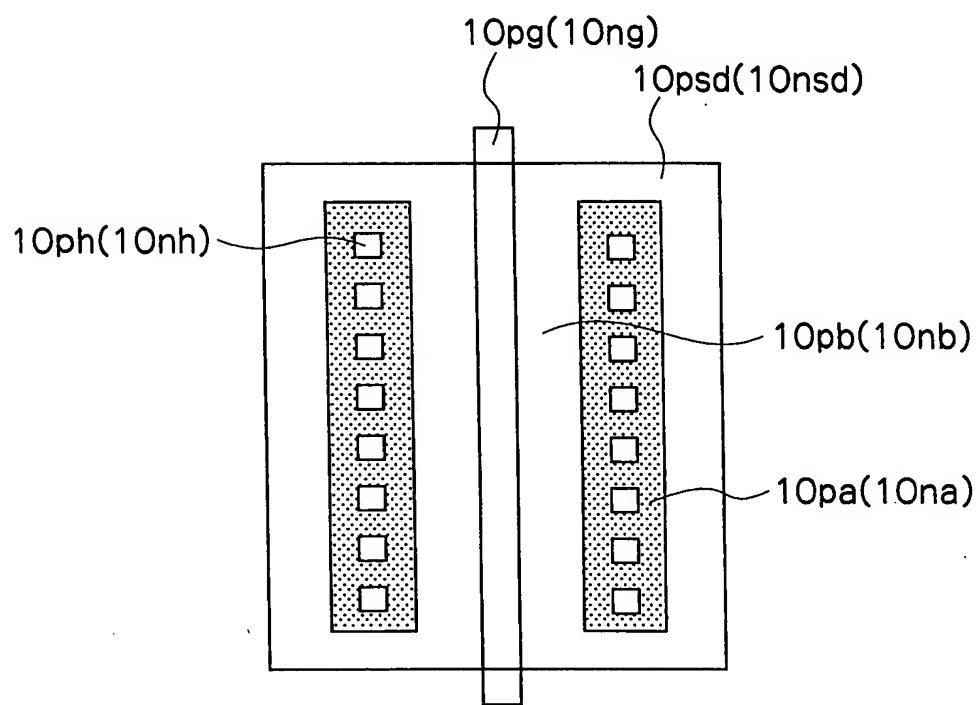


FIG.8



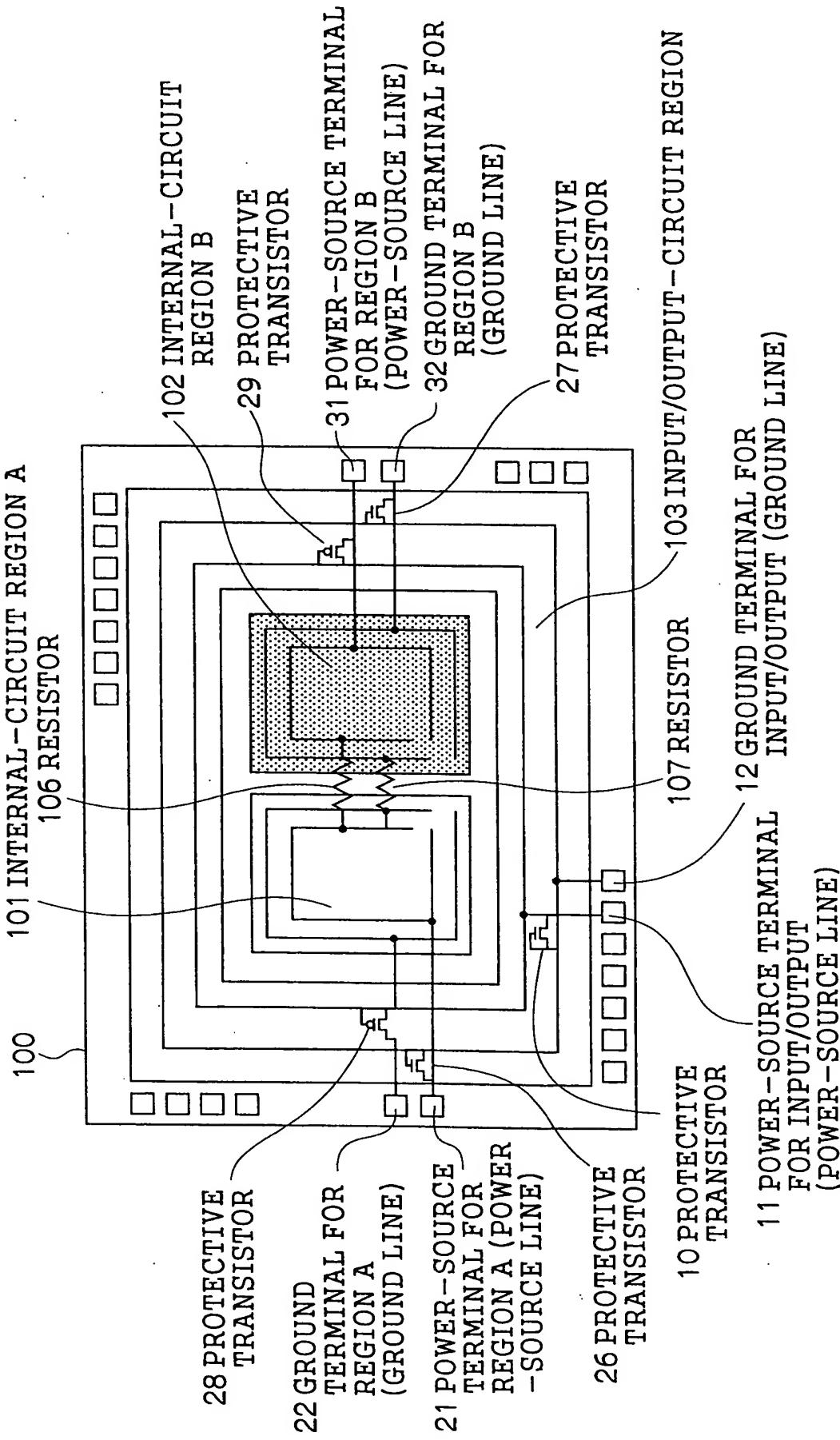
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Inventor: Katsuhiro KATO

FIG.9



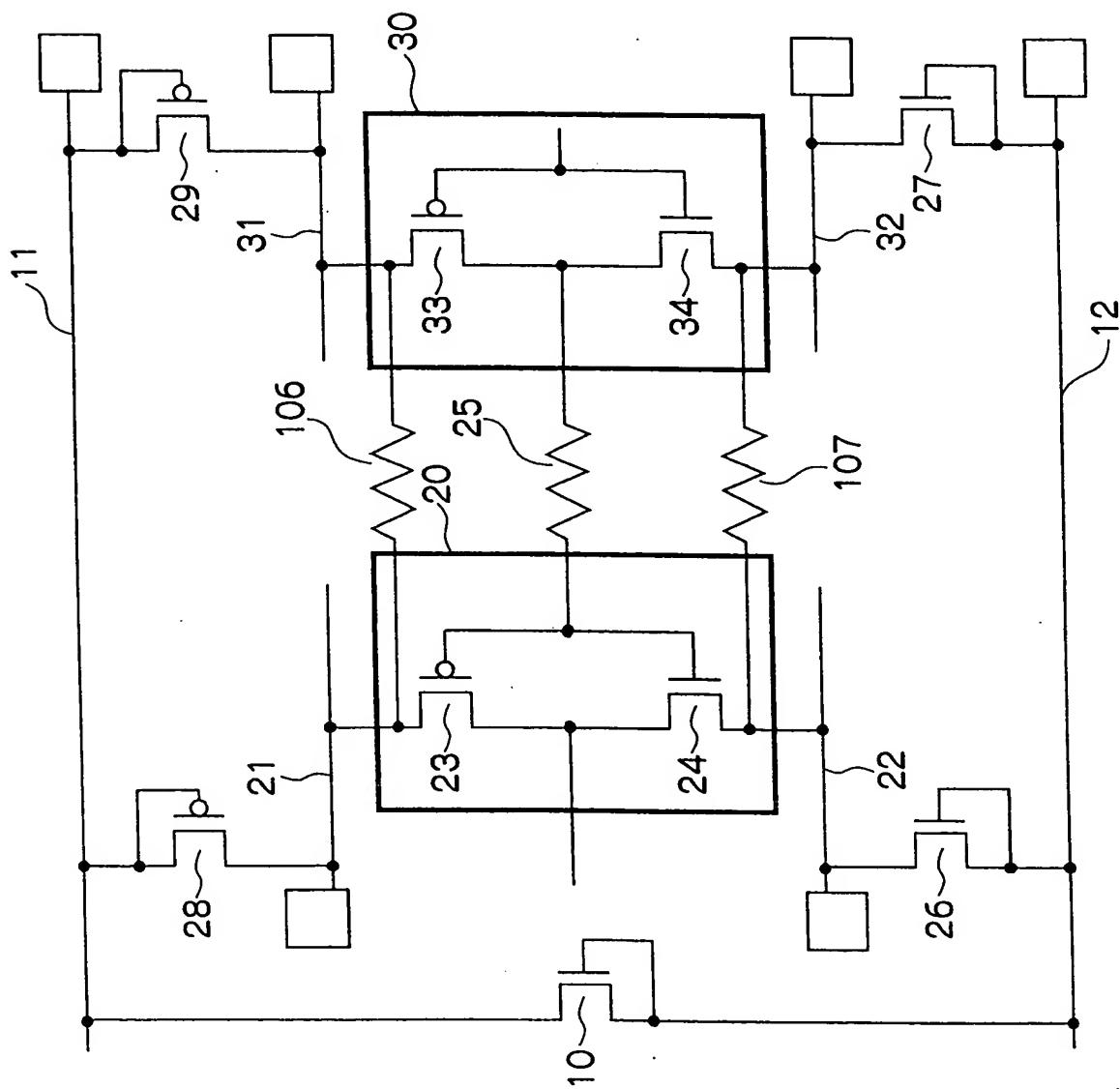
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Inventor: Katsuhiro KATO

FIG.10



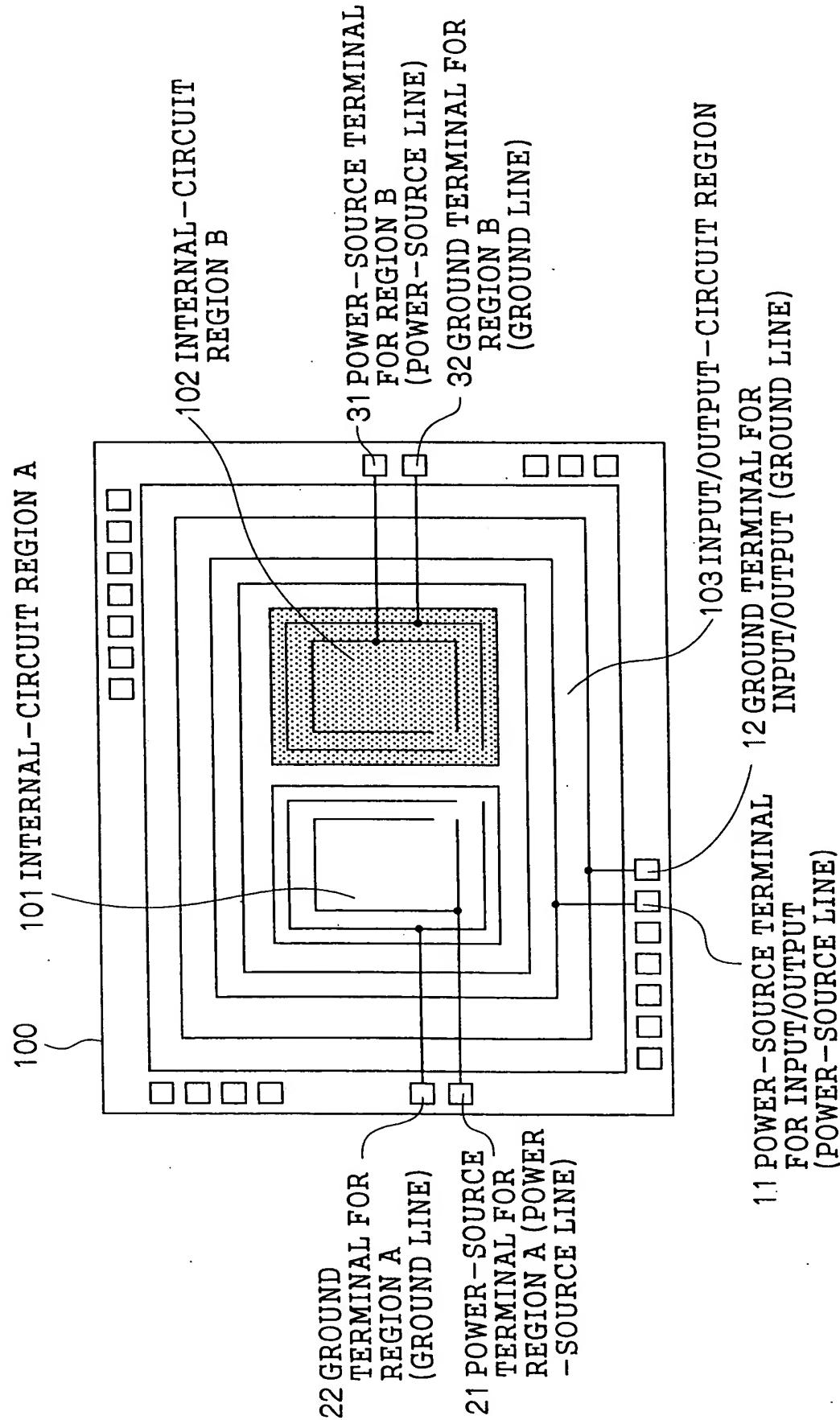
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Application No. [New] – Attorney Doc. No. OKI.439D  
Inventor: Katsuhiro KATO

FIG.11



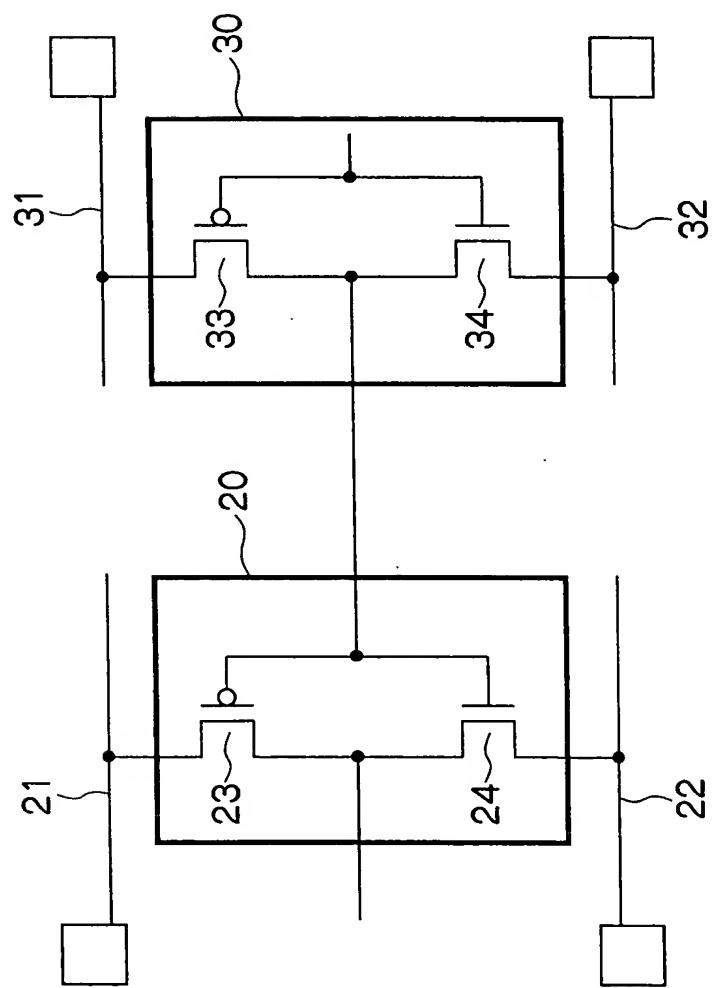
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Inventor: Katsuhiro KATO

FIG.12



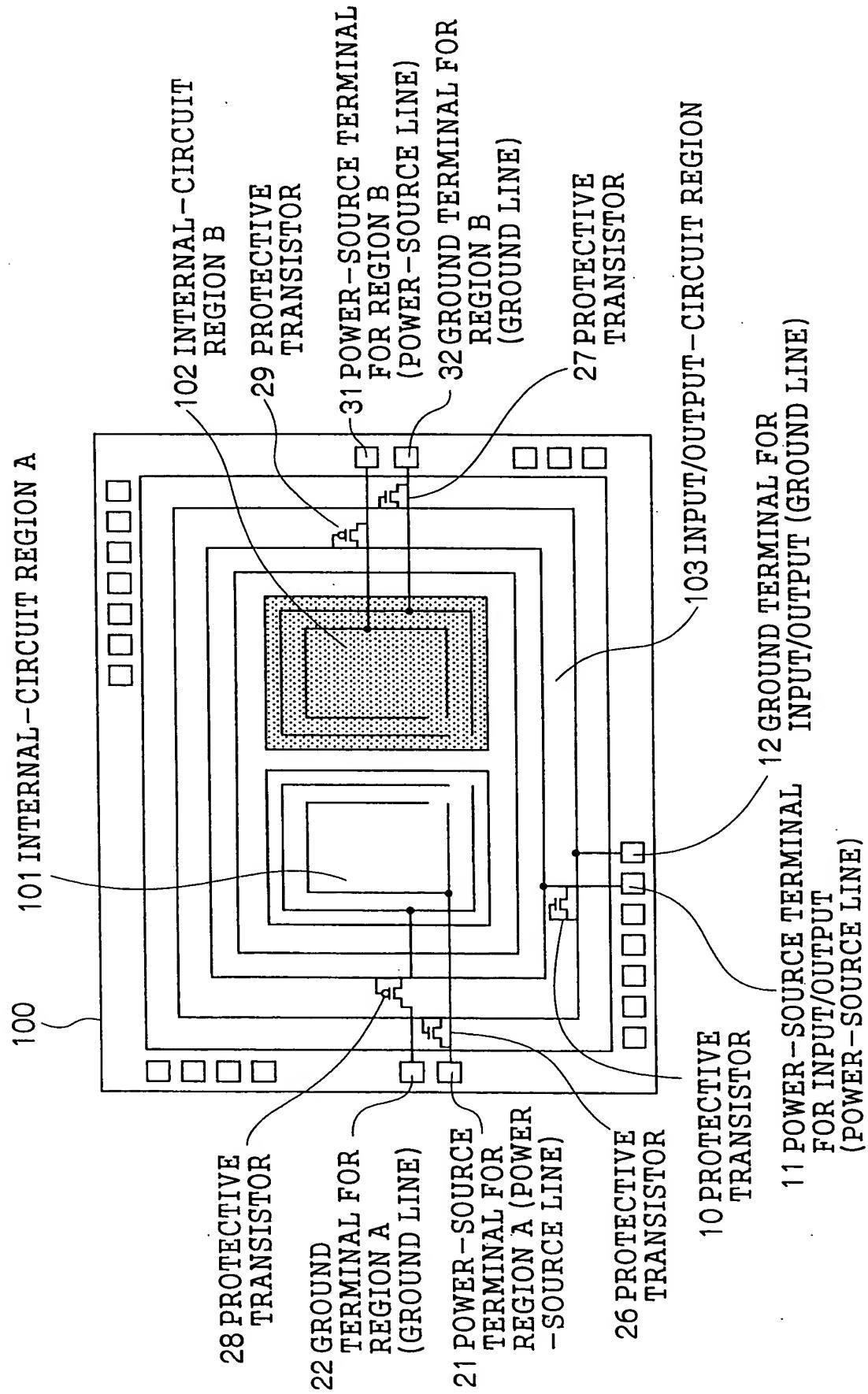
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Inventor: Katsuhiro KATO

FIG.13  
PRIOR ART



ELECTROSTATIC-BREAKDOWN-PREVENTIVE AND  
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Inventor: Katsuhiro KATO

FIG.14  
PRIOR ART



ELECTROSTATIC-BREAKDOWN-PREVENTIVE AND  
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Inventor: Katsuhiro KATO

FIG.15  
PRIOR ART

